

# RJK0202DSP

# Silicon N Channel Power MOS FET Power Switching

R07DS0238EJ0220 Rev.2.20 Jan 05, 2011

#### **Features**

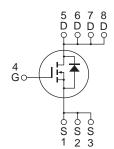
- High speed switching
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)}\!=5.0~\text{m}\Omega$  typ. (at  $V_{GS}\!=4.5~\text{V})$ 

#### **Outline**

RENESAS Package code: PRSP0008DD-D (Package name: SOP-8<FP-8DAV>)





1, 2, 3 Source

4 Gate 5, 6, 7, 8 Drain

### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	20	V
Gate to source voltage	$V_{GSS}$	±12	V
Drain current	I <sub>D</sub>	16	Α
Drain peak current	I <sub>D(pulse)</sub> Note1	128	Α
Body-drain diode reverse drain current	I <sub>DR</sub>	16	Α
Avalanche current	I <sub>AP</sub> Note 2	15	Α
Avalanche energy	E <sub>AR</sub> Note 2	45	mJ
Channel dissipation	Pch Note3	2.0	W
Channel to ambient thermal impedance	θch-a Note3	62.5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10s

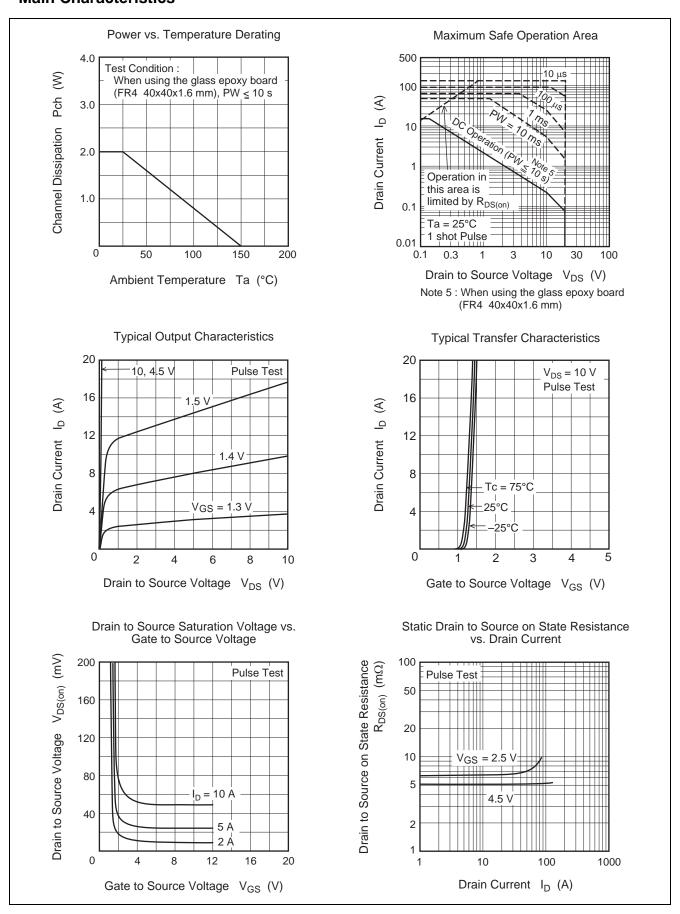
## **Electrical Characteristics**

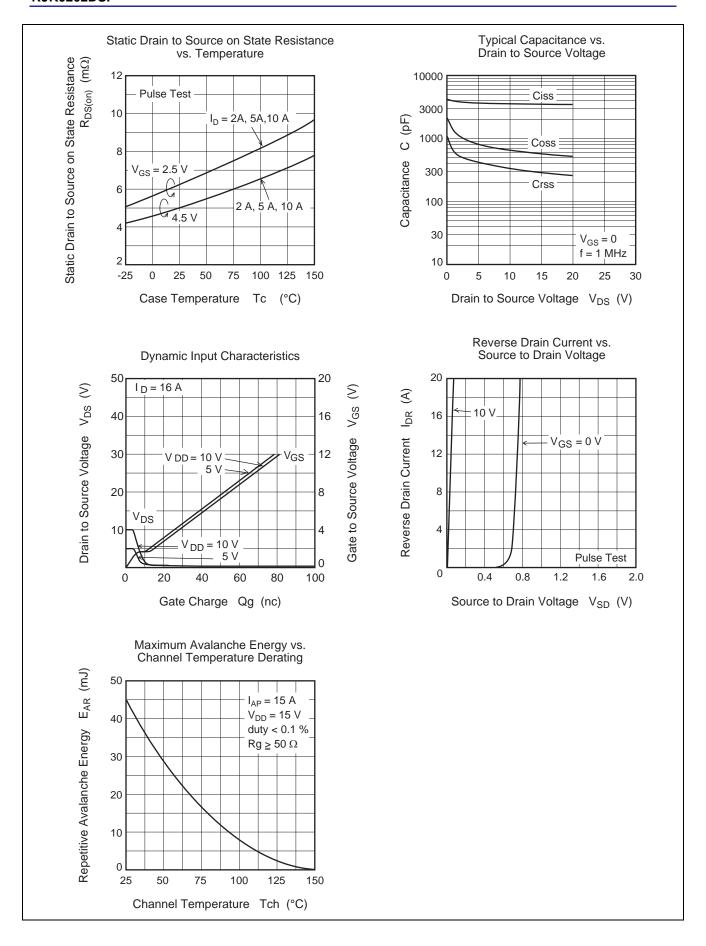
 $(Ta = 25^{\circ}C)$ 

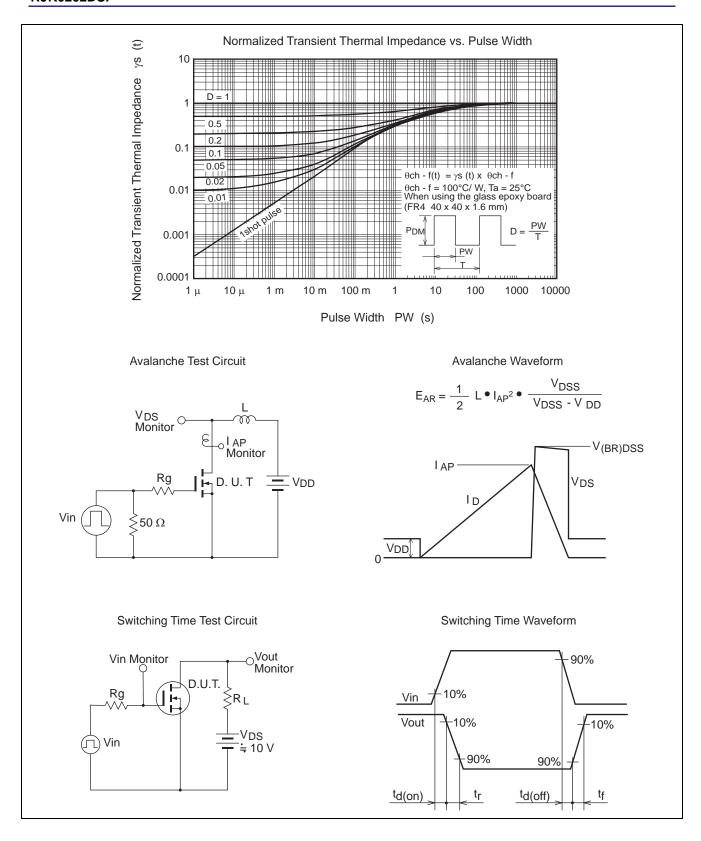
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	20	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	_	_	± 0.1	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μА	$V_{DS} = 20 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	_	1.4	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	5.0	6.3	mΩ	$I_D = 8 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	6.2	8.7	mΩ	$I_D = 8 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	_	55	_	S	$I_D = 8 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	3650	_	рF	$V_{DS} = 10 \text{ V}$
Output capacitance	Coss	_	640	_	рF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	330	_	рF	
Gate Resistance	Rg	_	1.4	_	Ω	
Total gate charge	Qg	_	31	_	nC	V <sub>DD</sub> = 10 V V <sub>GS</sub> = 4.5 V I <sub>D</sub> = 16 A
Gate to source charge	Qgs	_	6.2	_	nC	
Gate to drain charge	Qgd	_	6.9	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	20	_	ns	$V_{GS}$ = 4.5 V, $I_D$ = 8 A $V_{DD} \cong$ 10 V $R_L$ = 1.25 $\Omega$ $Rg$ = 4.7 $\Omega$
Rise time	t <sub>r</sub>	_	40	_	ns	
Turn-off delay time	$t_{d(off)}$	_	72	_	ns	
Fall time	t <sub>f</sub>	_	18	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.75	0.98	V	IF = 16 A, V <sub>GS</sub> = 0 Note4
Body-drain diode reverse recovery	t <sub>rr</sub>	_	50	_	ns	IF = 16 A, V <sub>GS</sub> = 0
time						$di_F/dt = 100 A/ \mu s$

Notes: 4. Pulse test

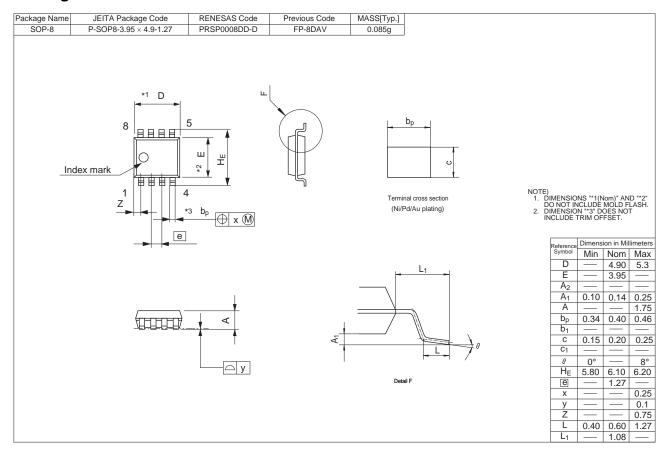
#### **Main Characteristics**







### **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK0202DSP-00-J0	2500 pcs	Taping

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